

Palo Alto Electron

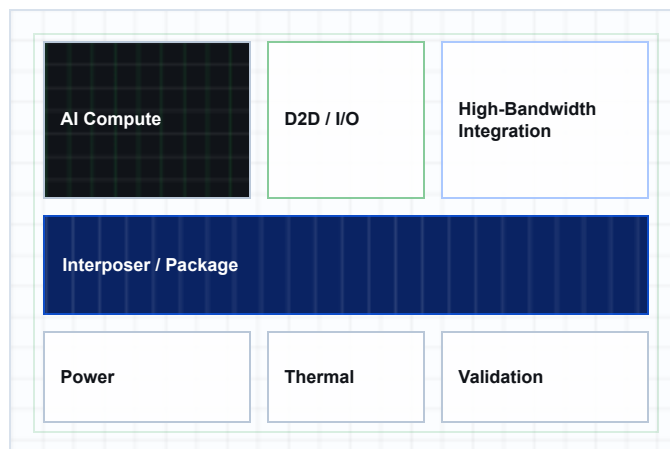
Advanced AI Hardware Engineering

Silicon • Chiplets • Packaging • Boards • Deployment-Ready Systems

Palo Alto Electron designs and executes advanced AI-native hardware systems spanning silicon, chiplets, packaging, boards, and deployment-ready infrastructure.

From early architecture through manufacturing coordination, we help customers build next-generation compute platforms for data center, edge, and trusted systems applications.

With deep expertise in chiplet integration, advanced packaging, system engineering, and Open Chiplet Economy leadership, Palo Alto Electron reduces execution risk and accelerates the path from concept to deployed hardware.



CORE CAPABILITIES

Engineering execution

- AI hardware architecture
- Chiplet and package design
- Interposer, substrate, and board-level integration
- SI / PI / PDN analysis
- Power and thermal modeling
- Firmware and bring-up
- System-level modeling
- Validation and diagnostics
- Manufacturing coordination

ENGAGEMENT AREAS

Customer programs

- Architecture review
- Feasibility study
- Chiplet/package program support
- Board/platform engineering
- Bring-up and validation
- Government / defense / lab collaboration
- Strategic ecosystem collaboration

Build advanced AI hardware with us.

info@paloaltoelectron.com
paloaltoelectron.com